

FIG. 1  
CONVENTIONAL ART

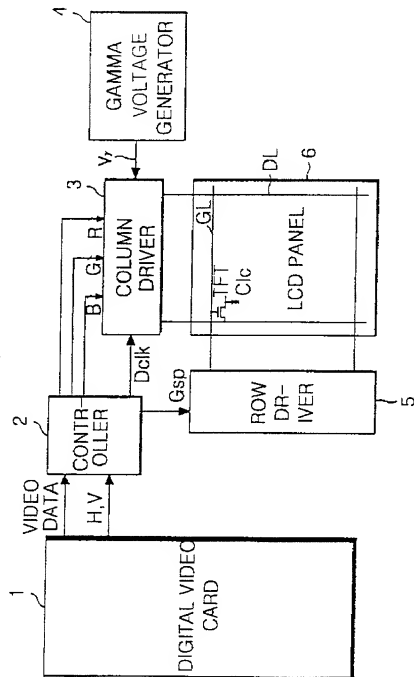


FIG. 2  
CONVENTIONAL ART

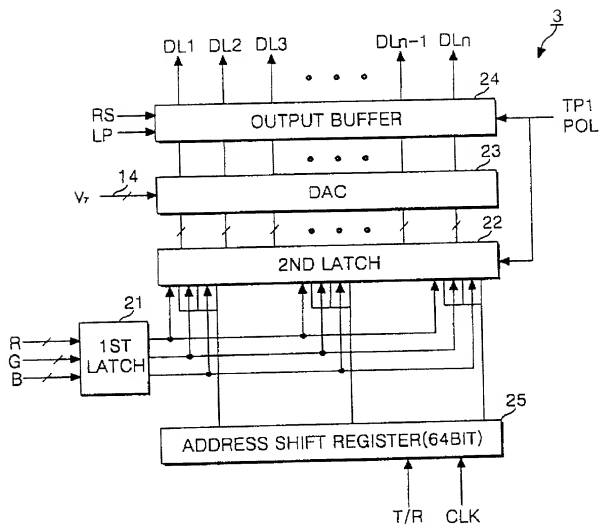
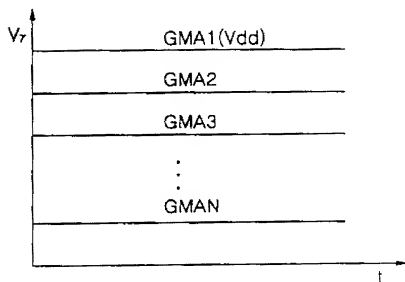
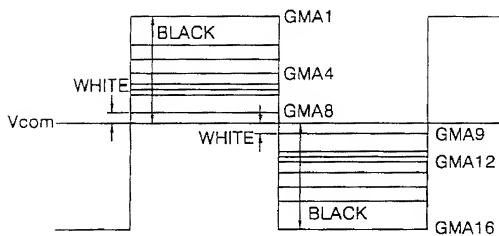


FIG.3  
CONVENTIONAL ART



09892997-062801

FIG. 4  
CONVENTIONAL ART



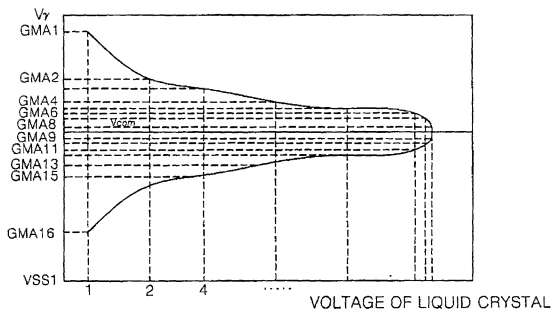
[illegible]

FIG. 6  
CONVENTIONAL ART

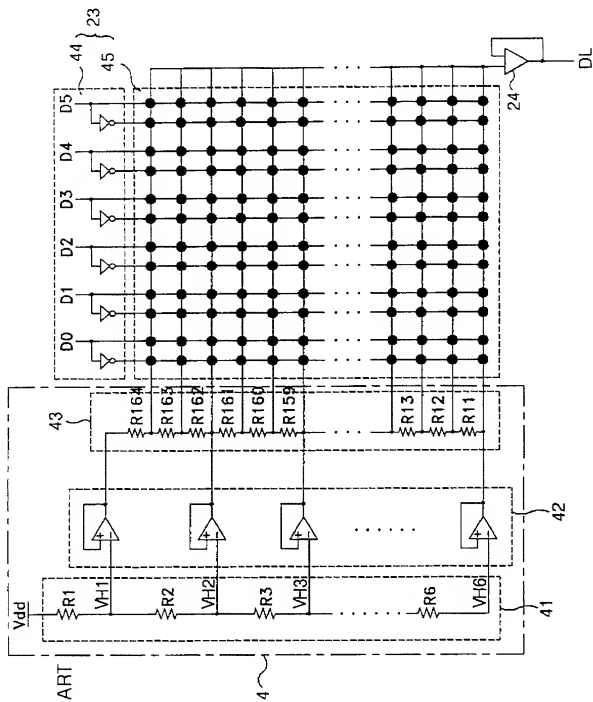
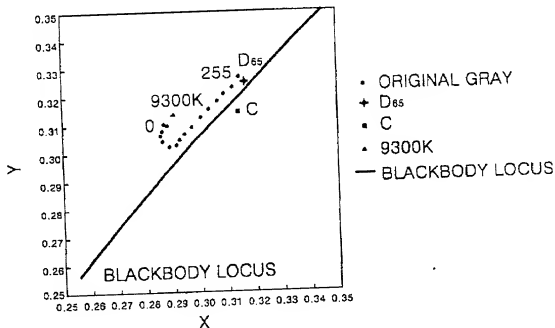
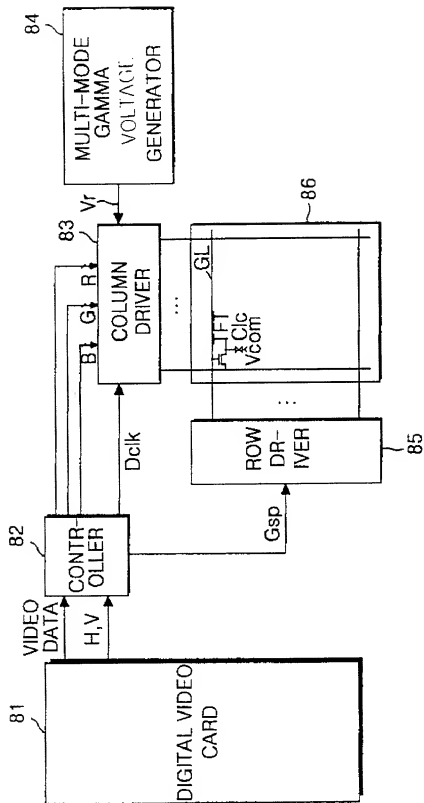


FIG. 7  
CONVENTIONAL ART



00000000.000000

FIG. 8





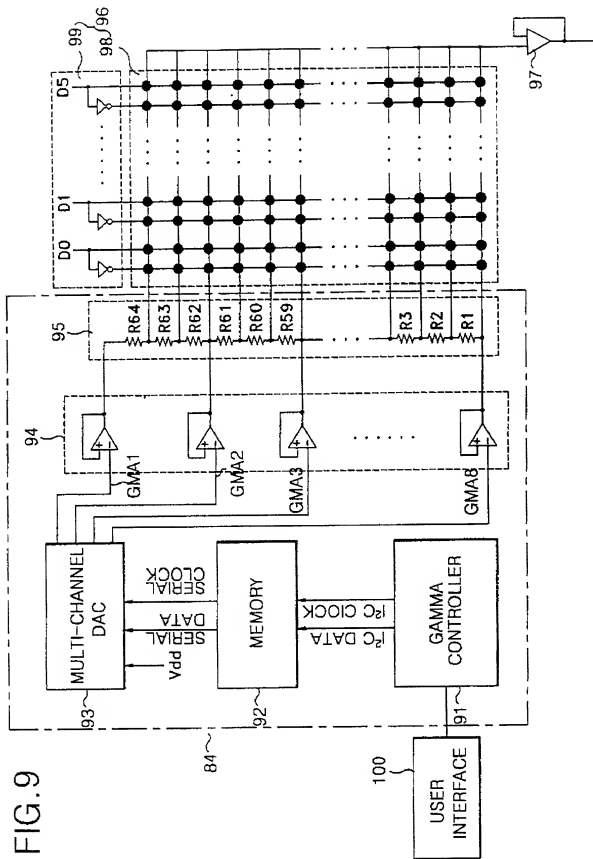


FIG.10

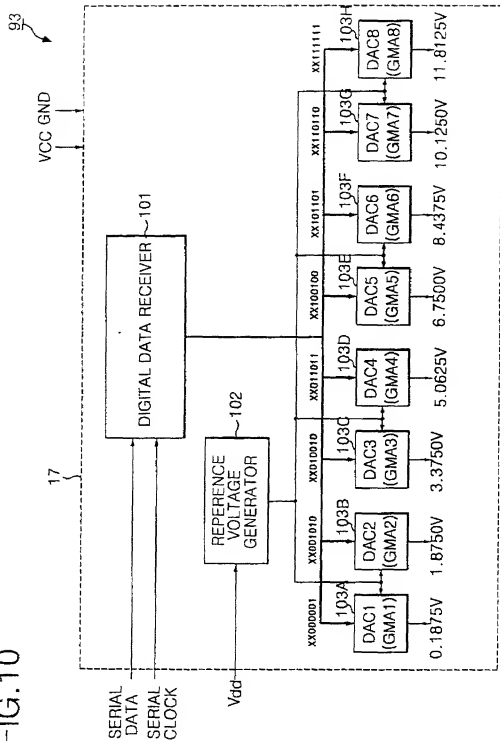


FIG.11

S	A3 A2 A1 A0 SD SC SB SA	A	X X D5 D4 D3 D2 D1 D0
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S=START CONDITION ,A3~A0=ADDRESS BIT, SC~SA=SUBADDRESS BIT  
A=HEADER BIT, D5~D0=DATA BIT

FIG. 12

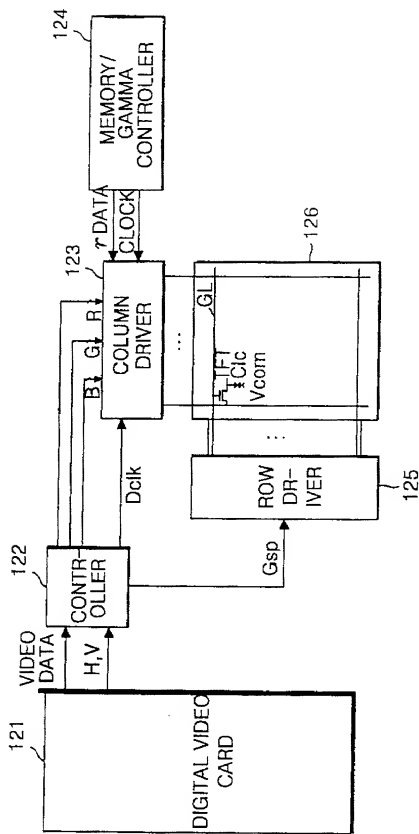


FIG. 13

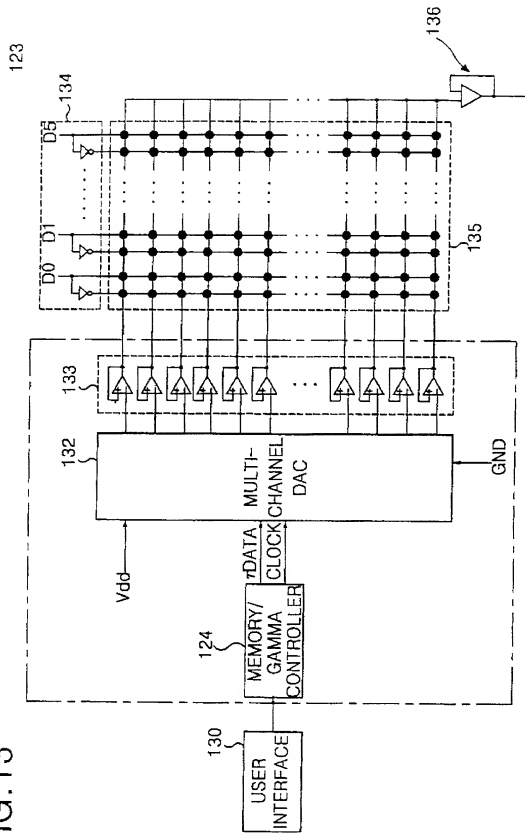


FIG.14

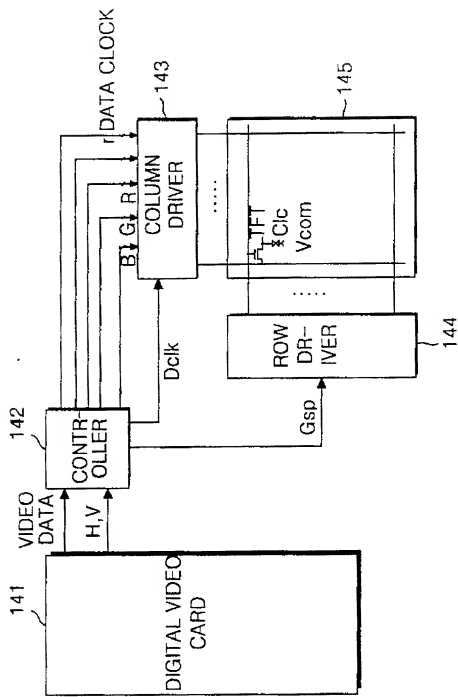


FIG. 15

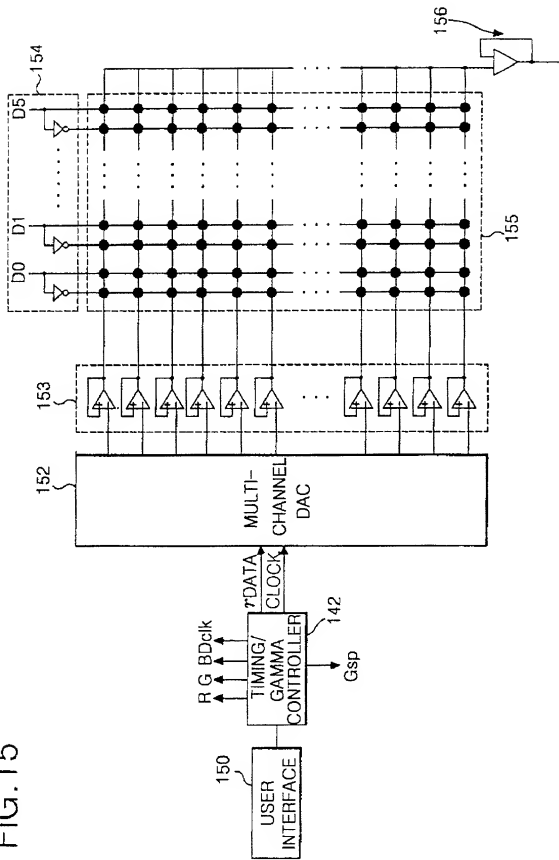


FIG. 16

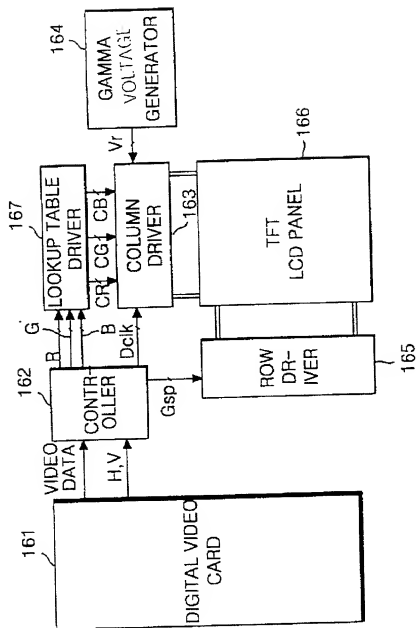




FIG.17

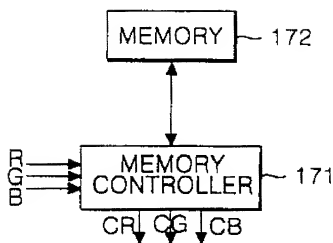


FIG.18

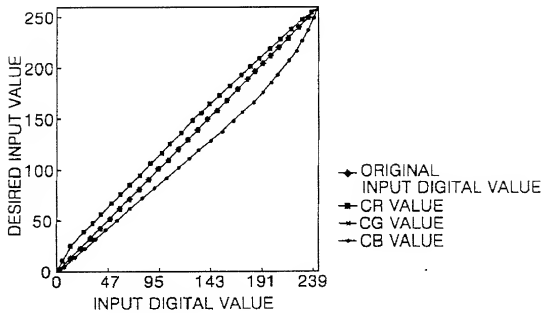


FIG.19

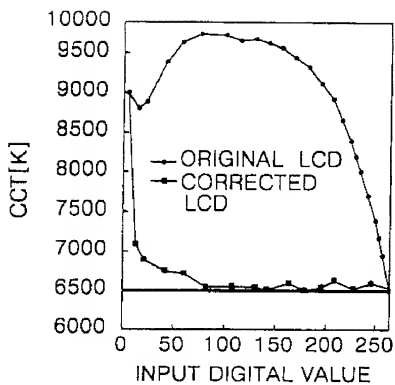
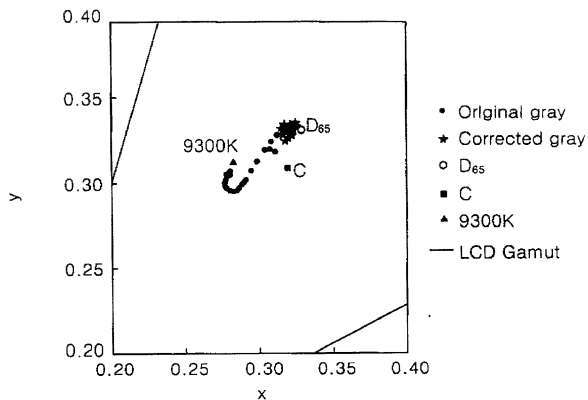
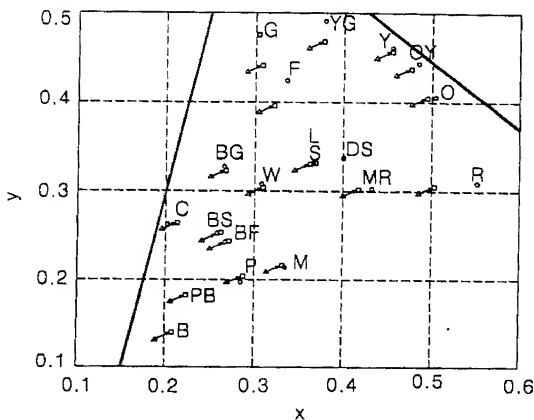


FIG.20



# FIG.21



- CHROMINANCE CO-ORDINATES OF INPUT IMAGE
- ▲ CHROMINANCE CO-ORDINATES OF LCD DISPLAY IMAGE

- ◻ CHROMINANCE CO-ORDINATES CORRECTED BY LOOKUP TABLE